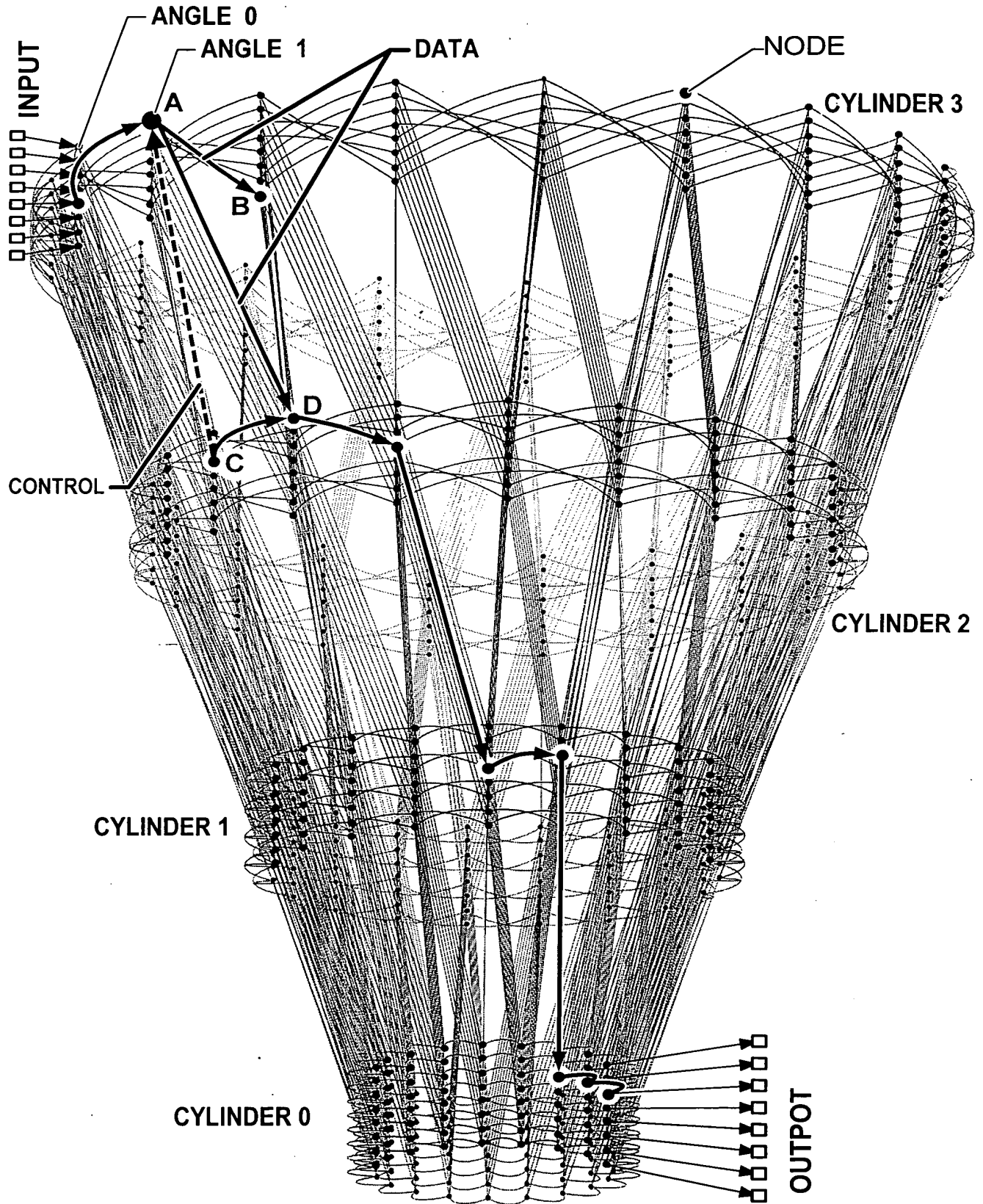


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MULTIPLE LEVEL, MINIMUM LOGIC NETWORK

Fig 1A

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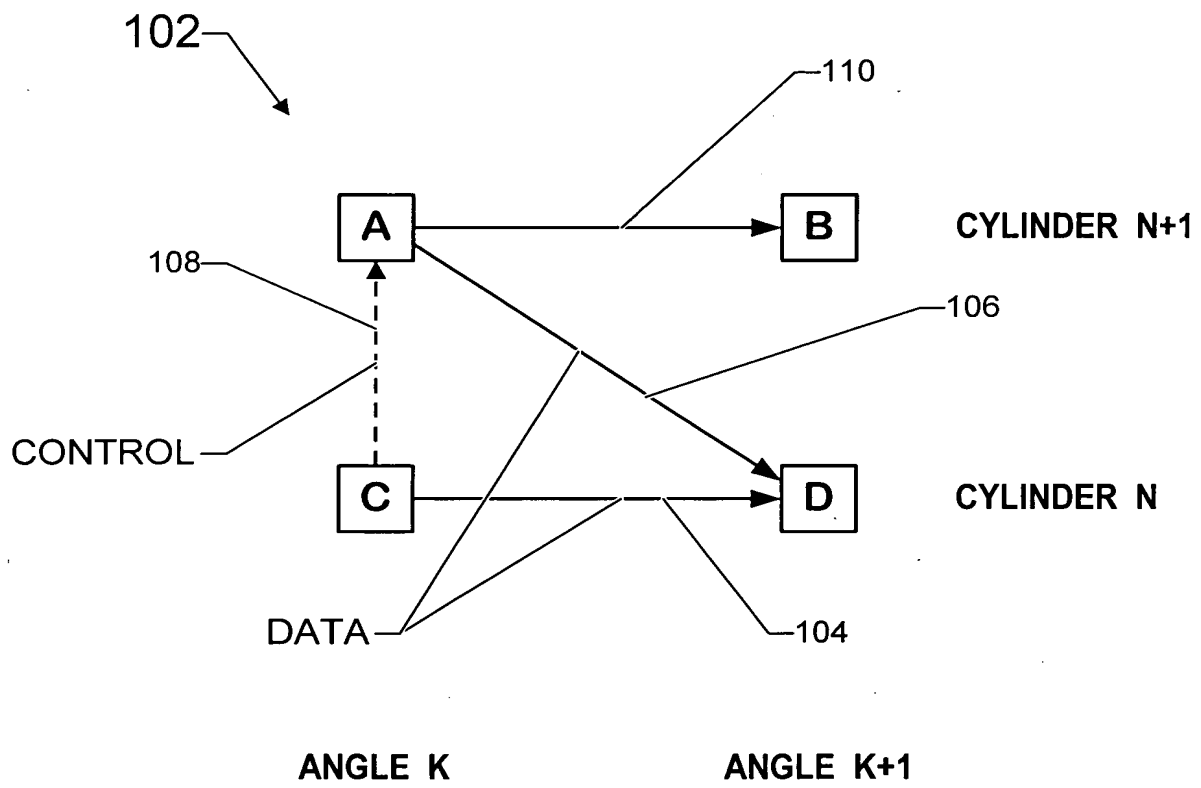


Fig 1B

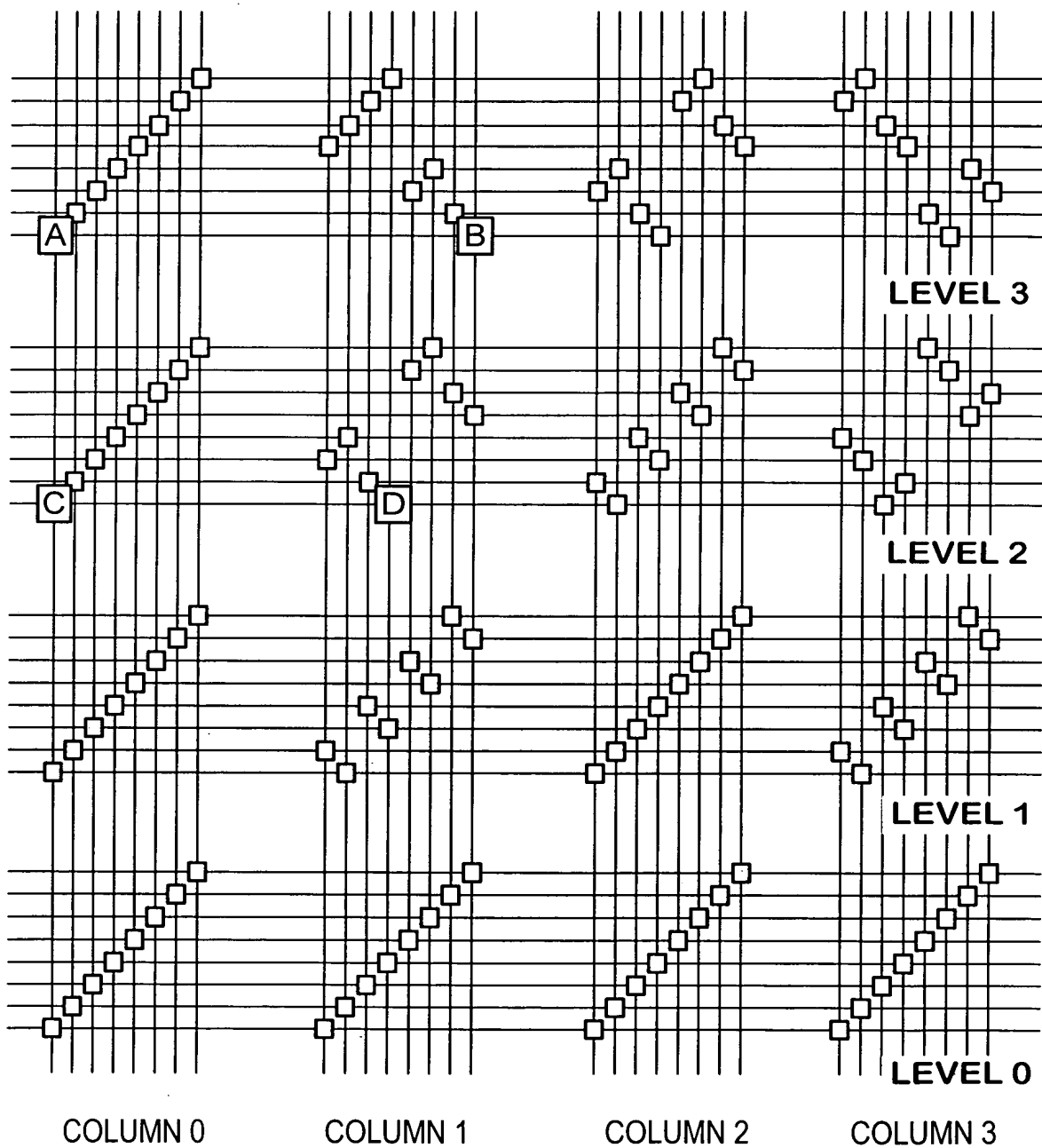


Fig 2A

Fig 2B

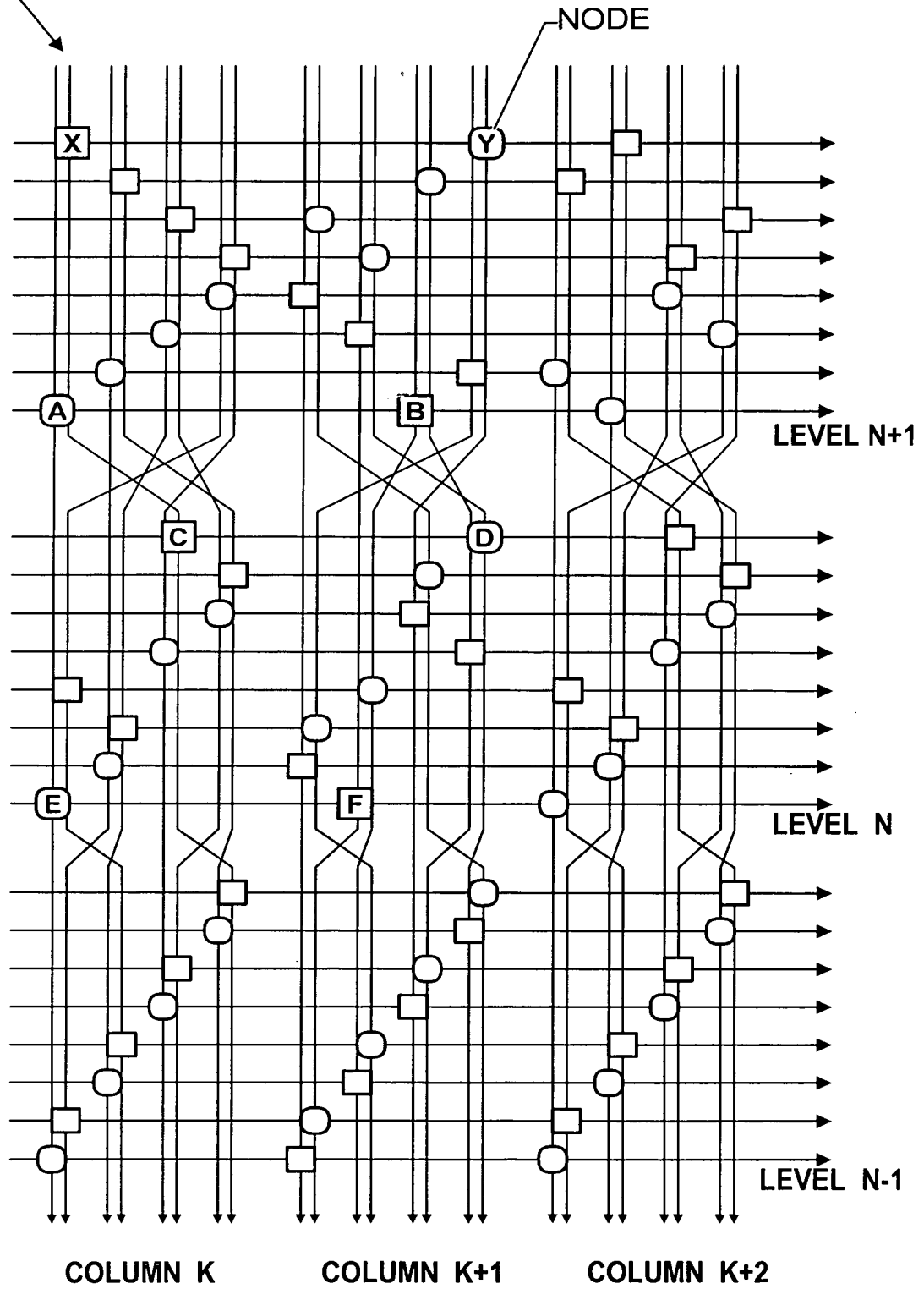


Fig 3A

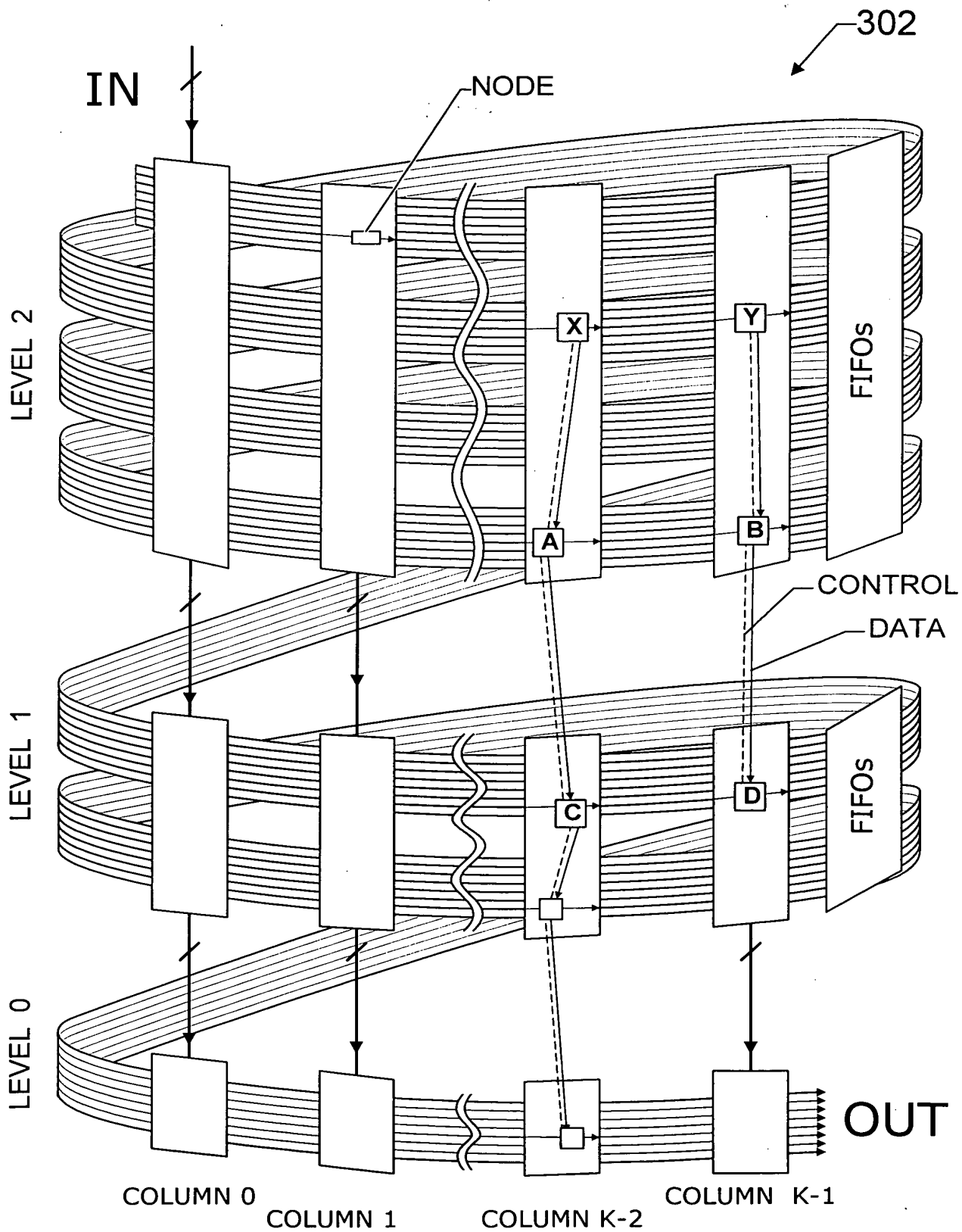


Fig 3B

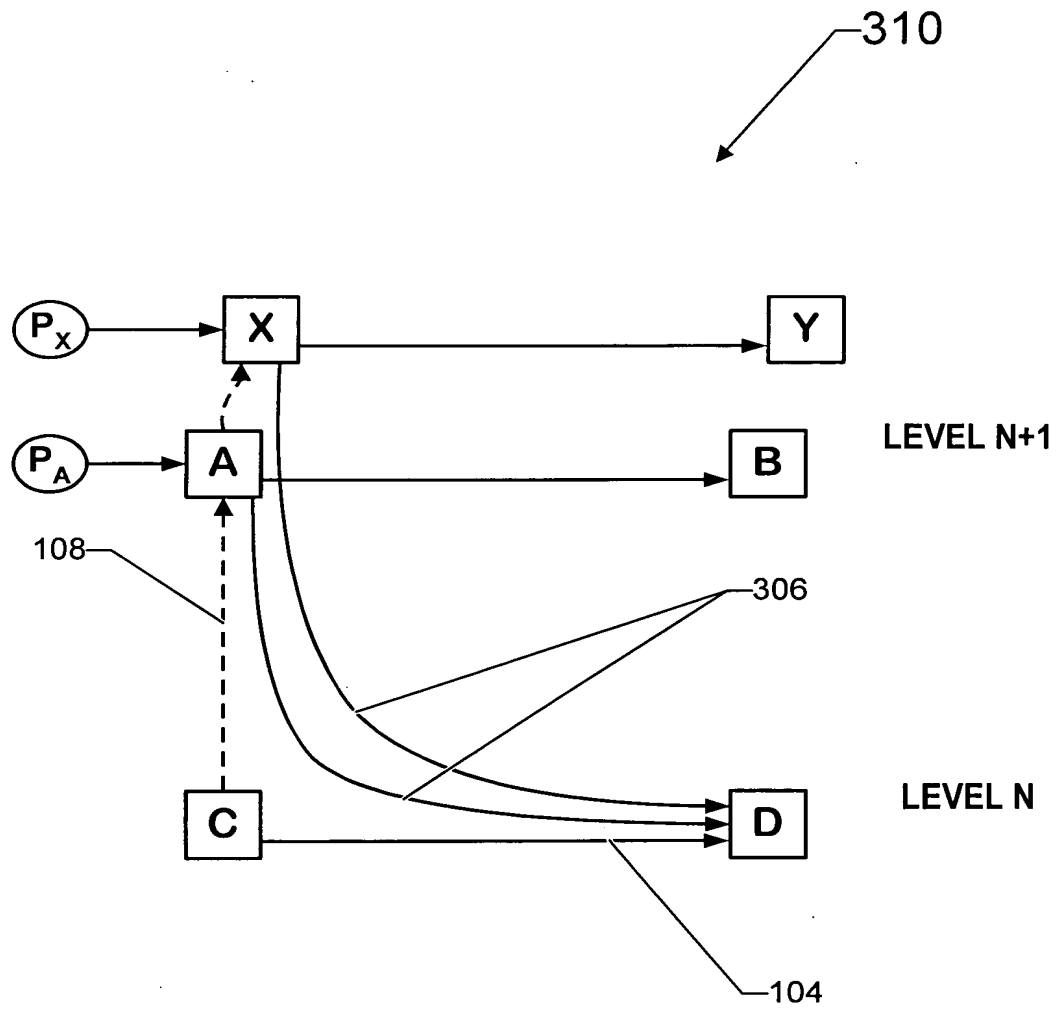


Fig 3C

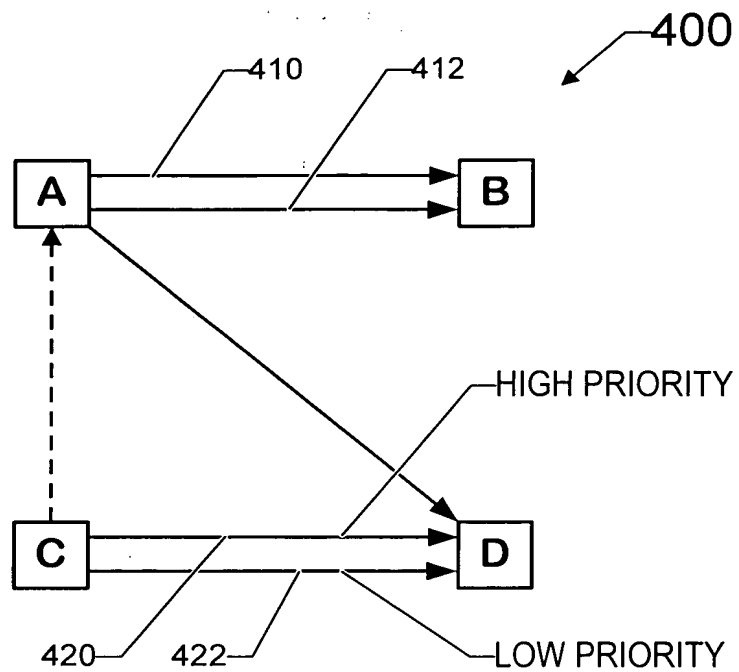


Fig 4A

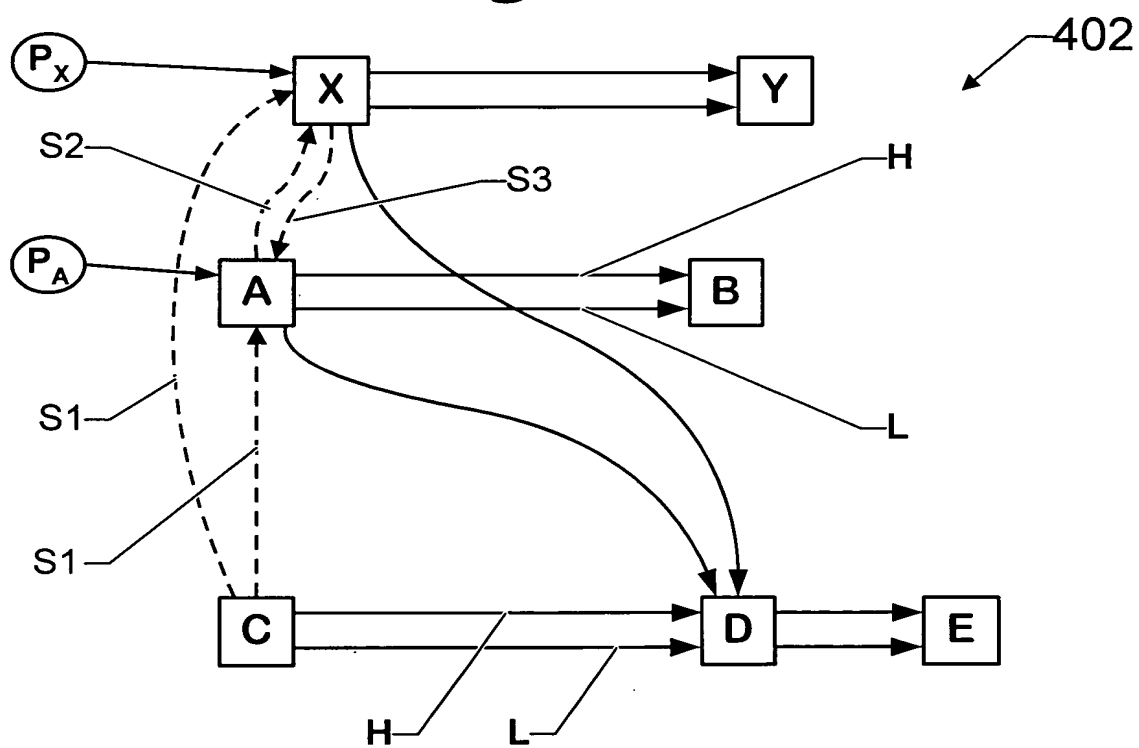


Fig 4B

Fig 4C

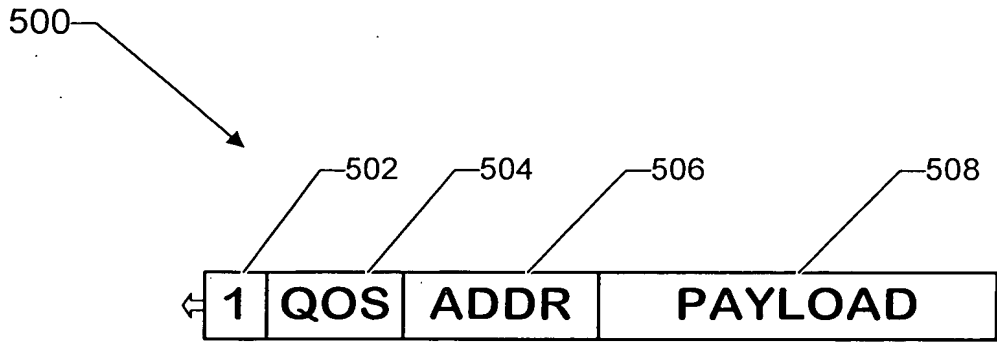


Fig 5A

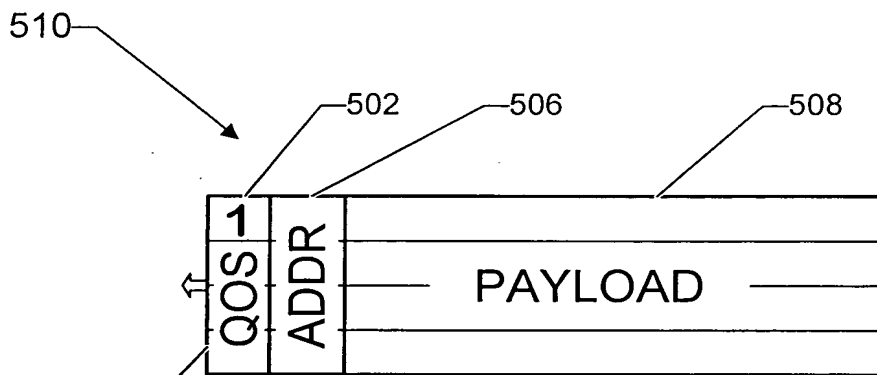


Fig 5B

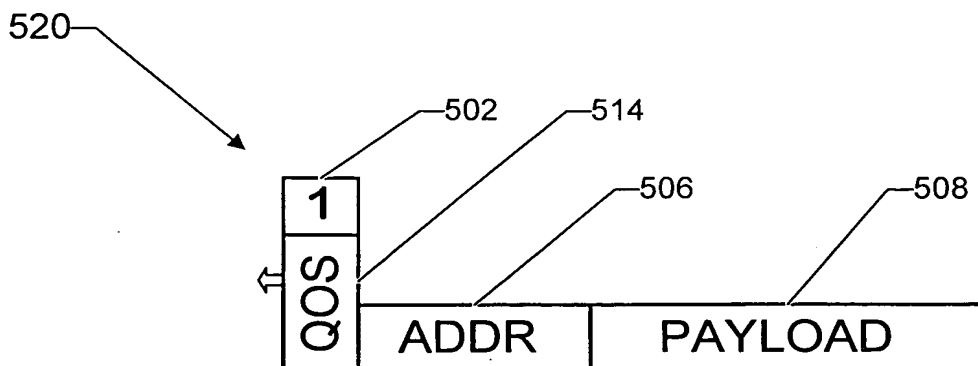


Fig 5C